



PTO/SB/08B (08-03)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	10/734,227		
		Filing Date	12/15/2003		
		First Named Inventor	Sang Don LEE		
		Art Unit	<del>2815</del> 2812		
Sheet	1	of	1	Examiner Name	<del>Eugene LEE</del> Cheung Lee
				Attorney Docket Number	40296-0053

NON PATENT LITERATURE DOCUMENTS			
Examiner initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Schwalke, Udo, "EXTICATE: The Ultimate Process Architecture for Submicron CMOS Technologies", IEEE TRANSACTIONS ON ELECTRON DEVICES, VOL. 44, NO. 11, November 1997.	

Examiner Signature	<i>Cheung Lee</i>	Date Considered	8/25/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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